5E6025

Roll No.

Total No of Pages: 3

6E6025

B.Tech. VI-Sem. (Main/Back) Exam., April/May-2016 Computer Science & Engineering 6CS5A Embedded System Design

Time: 3 Hours

Maximum Marks: 80

Min. Passing Marks (Main & Back): 26

Instructions to Candidates:-

Attempt any five questions, selecting one question from each unit. All Questions carry equal marks. Schematic argrams must be shown wherever necessary. Any data you feel missing may suitably be assumed and stated clearly.

Units of quantities used/ calculated must be stated clearly.

Use of following supporting material is permitted during examination.

(Mentioned in form No. 200)

1.NIL

2. <u>NIL.</u>

<u>UNIT-I</u>

- Q.1 (a) What is embedded system? What are the hardware needs before designing an embedded system? [8]
 - (b) What is the difference between an embedded system and a general computing system? [8]

OR

Q.1 (a) What are programmable logic devices? Also explain field programmable gate arrays and complete programmable logic devices. [8]

(b)	What are the advantages of programmable logic devises over fix	ed logic	
	devices?	[8]	
	<u>UNIT-II</u>		
Q.2 (a)	Explain level triggered and edge triggered interrupts. Which one	of these	
	interrupts in generally recommended for interrupt signals that are very	short or	
	very long?	[8]	
(b)	What are interrupt service routines (ISRs) and how ISRs handle a interru	pt? [8]	
	<u>OR</u>		
Q.2 (a)	What is context and why context saving occurs in a multitasking system	? [8]	
(b)	What operations are performed by CPU for solving shared data problems	s? [8]	
	<u>UNIT-</u> III		
Q.3 (a)	What is real time operating system? Categorizeit, also write down its bei	nefits. [8]	
(b)	Describe the various states of tasks with respect to RTOS. Also explain	n the role	
	of timer function in RTOS	[8]	
<u>OR</u>			
Q.3 (a)	Write short note on the following: -	[8]	
	(i) Scheduler		
	(ii) Reentrancy.		
(b)	How inter task communication process is done by mailboxes and pipes?	[8]	
	<u>UNIT-IV</u>		
Q.4 (a)	Briefly describe the steps involved in embedded system development.	[8]	
(b)	Explain the parameters of real time task. Also enlist the types of task s	cheduling	
•	in RTOS.	[8]	
[6E6025]	Page 2 of 3	[6300]	

Q.4	(a)	Write short note on the following: -	[8]	
		(i) QNX		
		(ii) RT LINUX		
	(b)	What is real time system? Differentiate hard real time system and soft real	time	
		system.	[8]	
UNIT-V				
Q.5	(2)	Explain the process of software code development in embedded system.	[8]	
Q.J	(a)			
	(b)	Explain the version techniques by which the code in uploaded to target board	i. [8]	
		<u>OR</u>		
Q.5	Wri	te short note on the followings: -	[16]	
	(a)	Logic Analyzer		
	(b)	In circuit Emulator (ICE)		
	(c)	Monitor		
	(d)	Issues with traditional Emulation.		
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Page 3 of 3

[6E6025]

[6300]